

AMENDMENT TRANSMITTAL LETTER (Small Entity)

Applicant(s): STERLING SMITH

Docket No.
MSS0007-US

Serial No.
10/663,755

Filing Date
September 27, 2003

Examiner
NGUYEN, Hiep

Group Art Unit
2816

Invention Disclosure Form INTERFACE CIRCUIT FOR CHIP

SEP 07 2004

TO THE ASSISTANT COMMISSIONER FOR PATENTS:

Transmitted herewith is an amendment in the above-identified application.

- Small Entity status of this application has been established under 37 CFR 1.27 by a verified statement previously submitted.
- A verified statement to establish Small Entity status under 37 FR 1.27 is enclosed.

The fee has been calculated and is transmitted as shown below.

CLAIMS AS AMENDED

	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST # PREV. PAID FOR	NUMBER EXTRA CLAIMS PRESENT	RATE	ADDITIONAL FEE
TOTAL CLAIMS	12 -	20 =	0	x \$9.00	\$0.00
INDEP. CLAIMS	2 -	3 =	0	x \$42.00	\$0.00
Multiple Dependent Claims (check if applicable)	<input type="checkbox"/>				\$0.00
				TOTAL ADDITIONAL FEE FOR THIS AMENDMENT	\$0.00

No additional fee is required for amendment.

Please charge Deposit Account No. _____ in the amount of _____
A duplicate copy of this sheet is enclosed.

A check in the amount of _____ to cover the filing fee is enclosed.

The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 50-1390
A duplicate copy of this sheet is enclosed.

Any additional filing fees required under 37 C.F.R. 1.16.

Any patent application processing fees under 37 CFR 1.17.

Dated: September 7, 2004

**Michael D. Bednarek
Registration No. 32,329
SHAW PITTMAN LLP
1650 Tysons Boulevard
McLean, VA 22102**

Customer No. 28970

CC:

I certify that this document and fee is being deposited on [REDACTED] with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

Signature of Person Mailing Correspondence

Typed or Printed Name of Person Mailing Correspondence

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Invention: ~~ON-CHIP~~ INTERFACE CIRCUIT FOR CHIP

SEP 07 2006

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